Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-17. (Canceled)

18. (Currently Amended) Method of addressing an array ~~a plurality~~ of microsystems which can be individually addressed by a control circuit, the control circuit and each microsystem including an ~~comprising~~ electromagnetic transmission means, and each microsystem further including ~~comprising~~ a counter, a register, and a read-only memory containing an identification code, the method comprising: ~~and having an addressing code,~~

an initialization phase, where the control circuit successively addresses the microsystems by their respective identification codes and stores a reduced addressing code in the respective registers of the microsystems; and

an addressing phase, where ~~of the microsystems comprising transmission, by~~ the control circuit transmits, ~~of~~ successive increment signals, and where the microsystems monitor ~~each microsystem monitoring~~ resetting of their respective counters ~~its counter~~ and, upon receipt of an increment signal, the microsystems increment a ~~incrementation of the~~ content of their respective counters and compare ~~its counter, and each microsystem comparing~~ the content and their respective reduced ~~of its counter and its~~ addressing code, so as to trigger execution of a pre-determined command when the content of the ~~its~~ counter and the reduced ~~its~~ addressing code are identical~~, method wherein, the microsystems forming an array of microsystems, each microsystem comprises an identification code, in a read-only memory, and the method comprises an initialization phase successively comprising, for each microsystem, addressing, by the control circuit, of the microsystem by its identification code and storing of a reduced addressing code supplied by the control circuit in a register of the microsystem~~.

19. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein the respective reduced addressing code of a microsystem is a function of its position in the array.

20. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein the reduced addressing codes of the microsystems correspond to increasing numbers ~~starting from a first microsystem~~.

21. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein the microsystems are arranged in lines and columns, the respective reduced addressing code of each microsystem comprising a line number and a column number respectively stored in line and column registers of the microsystem, ~~the~~ contents of the line and column registers being respectively compared with the contents of the line and column counters of the microsystems~~microsystem~~.

22. (Currently Amended) Method of addressing the array of microsystems according to claim 21, wherein the control circuit successively transmits line increment signals and column increment signals, the line increment signals causing the contents~~content~~ of the line counters to be incremented and the column increment signals causing the contents~~content~~ of the column counters to be incremented and the line counters ~~of all the microsystems~~ to be reset.

23. (Currently Amended) Method of addressing the array of microsystems according to claim 22, wherein the microsystems are arranged in lines, in columns and according to height, the respective reduced addressing code of each microsystem comprising an additional number associated to the height, stored in an additional register associated to the height, each microsystem comprising an additional counter associated to the height, contents ~~the content~~ of the register associated to the height being compared with the contents ~~content~~ of the counter associated to the height.

24. (Currently Amended) Method of addressing the array of microsystems according to claim 23, wherein the control circuit transmits height increment signals causing the additional counters associated to the height to be incremented and the line and column counters of all the microsystems to be reset.

25. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein a microsystem transmits an acquit signal after the execution of the command by the microsystem~~latter has executed its command~~.

26. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein the control circuit transmits data representative of ~~the~~ a type of command to be executed by the microsystems in association with transmission of a reset signal.

27. (Currently Amended) Method of addressing the array of microsystems according to claim 18, wherein the control circuit transmits data representative of ~~the~~ a type of command to be executed by the microsystems in association with transmission of an increment signal.